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APPLICANT
Combs, et al.

FILING DATE

GROUP

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLAS S	SUB-CLASS	FILING DATE IF APPROPRIATE
		6,058,496	05/2000	Gillis, et al.			
		5,923,676	07/1999	Sunter, et al.			
		5,736,862	04/1998	Hamblin			
		5,502,392	03/1996	Arjavalingam, et al.			
		5,278,841	01/1994	Myers			
		5,268,645	12/1993	Prokoff, et al.			
		5,266,901	11/1993	Woo			
		4,565,966	01/1986	Burr, et al.			
		6,397,361	5/2002	Saitoh, Toshiharu			
		6,2100,163	7/2001	Acroix et al.			
		6,020,752	2/2000	Shimasaki, N.			
		5,102,989	2/1997	Aria, Percy R.			
		5,241,261	8/1993	Nishimura, T.			

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLAS S	SUB-CLASS	TRANSLATION
						YES
						NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

IBM Technical Disclosure Bulletin, Vol. 37, No. 9, September 1994, "Interconnect Verification Method for Boards, pp. 299-302.

IBM Technical Disclosure Bulletin, Vol. 8, No. 11, April, 1966, "Measurement of Impedances Under Dynamic Pulse Conditions", pp. 1507-1510.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.